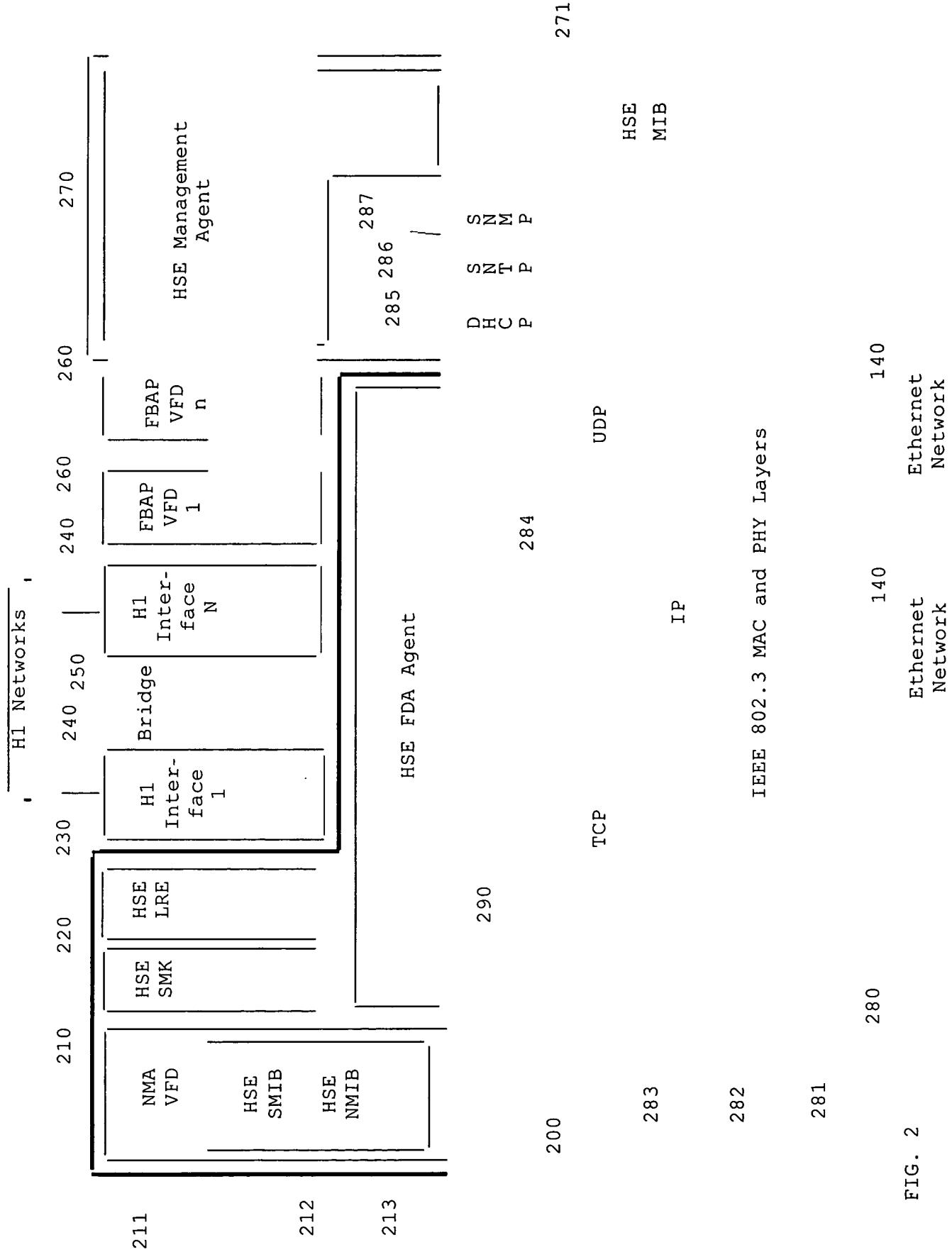


FIG. 1



## HSE MIB Structure

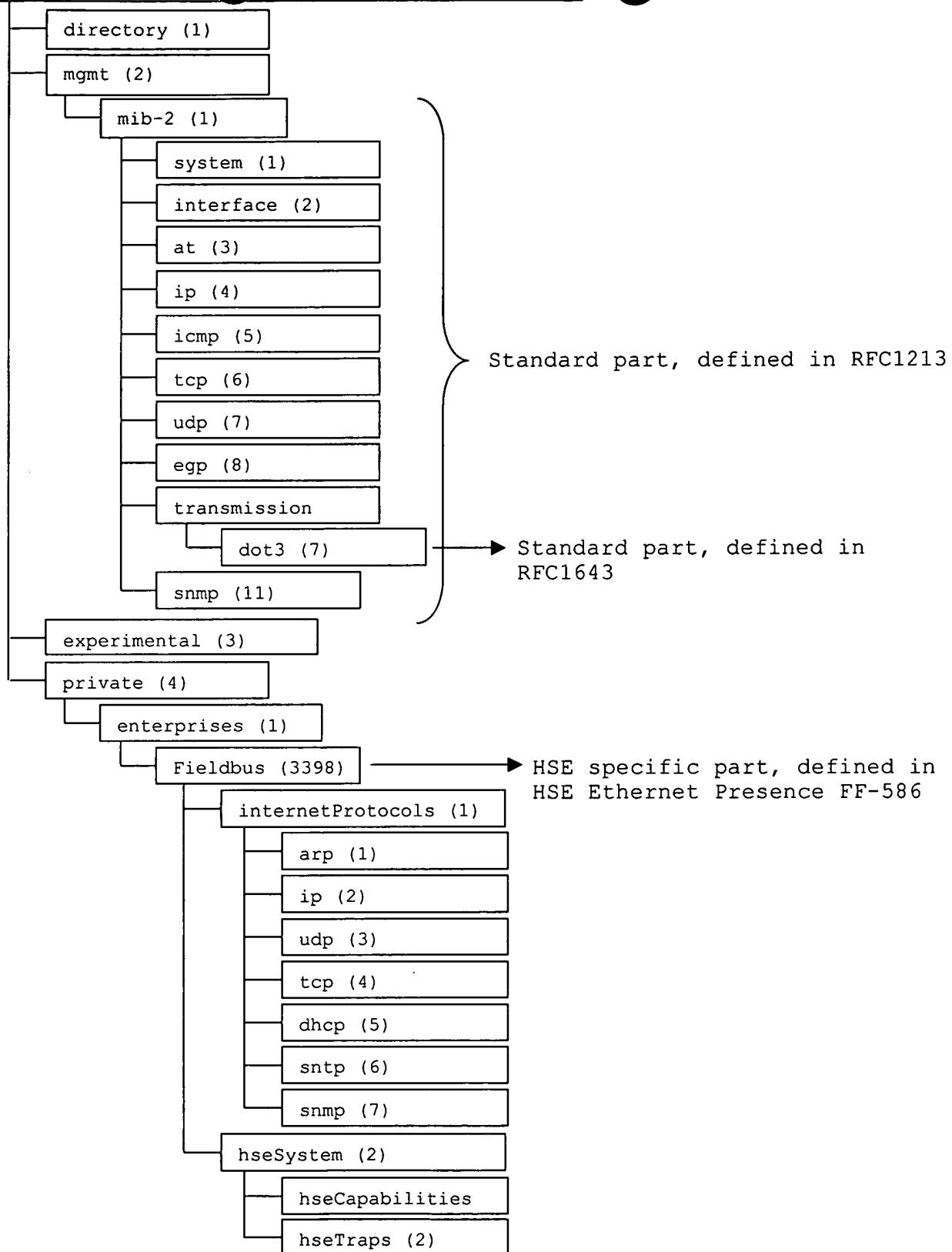


FIG. 3

□ OUT 230 " ZEUGS ETC

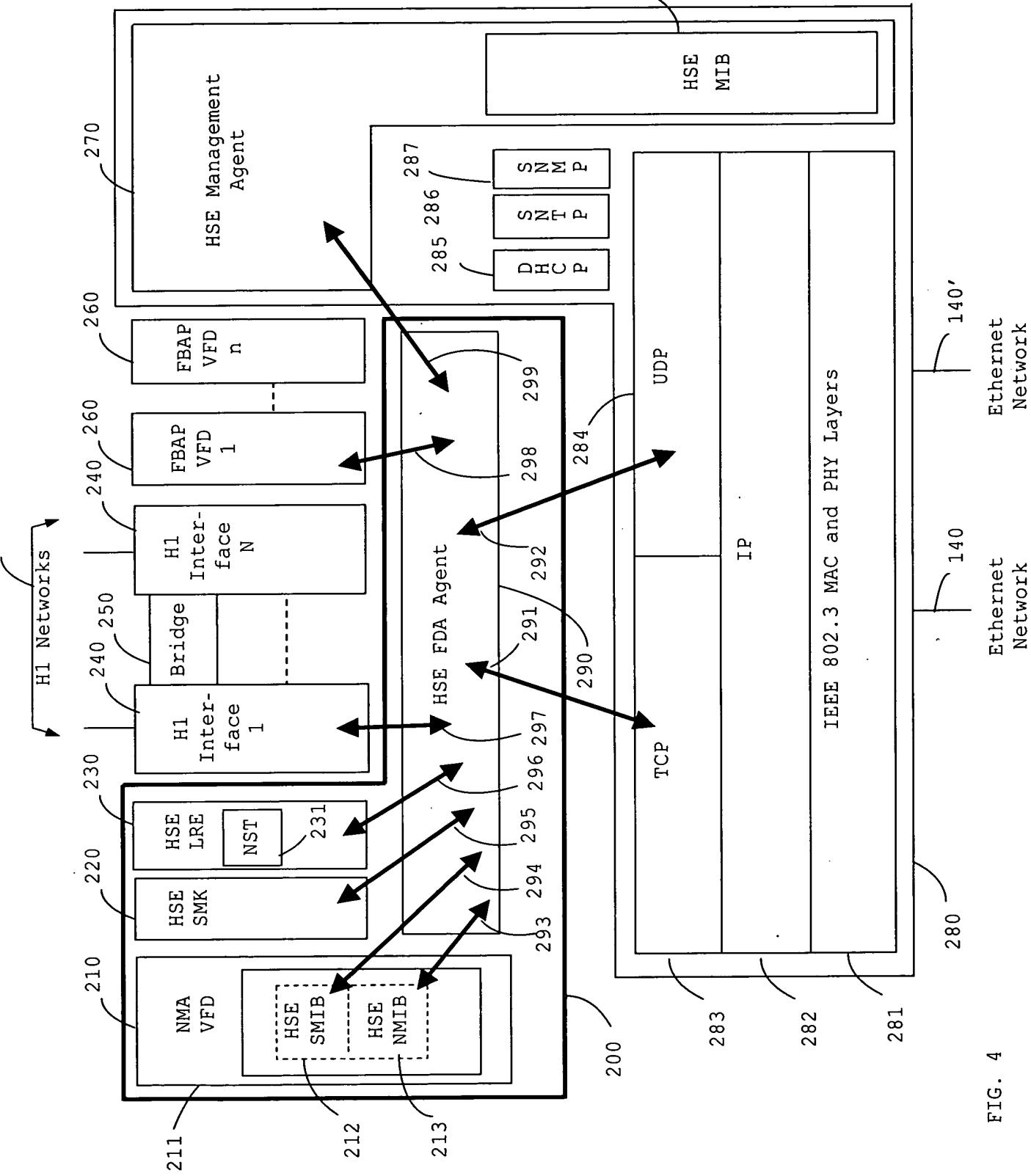


FIG. 4

Ethernet Network

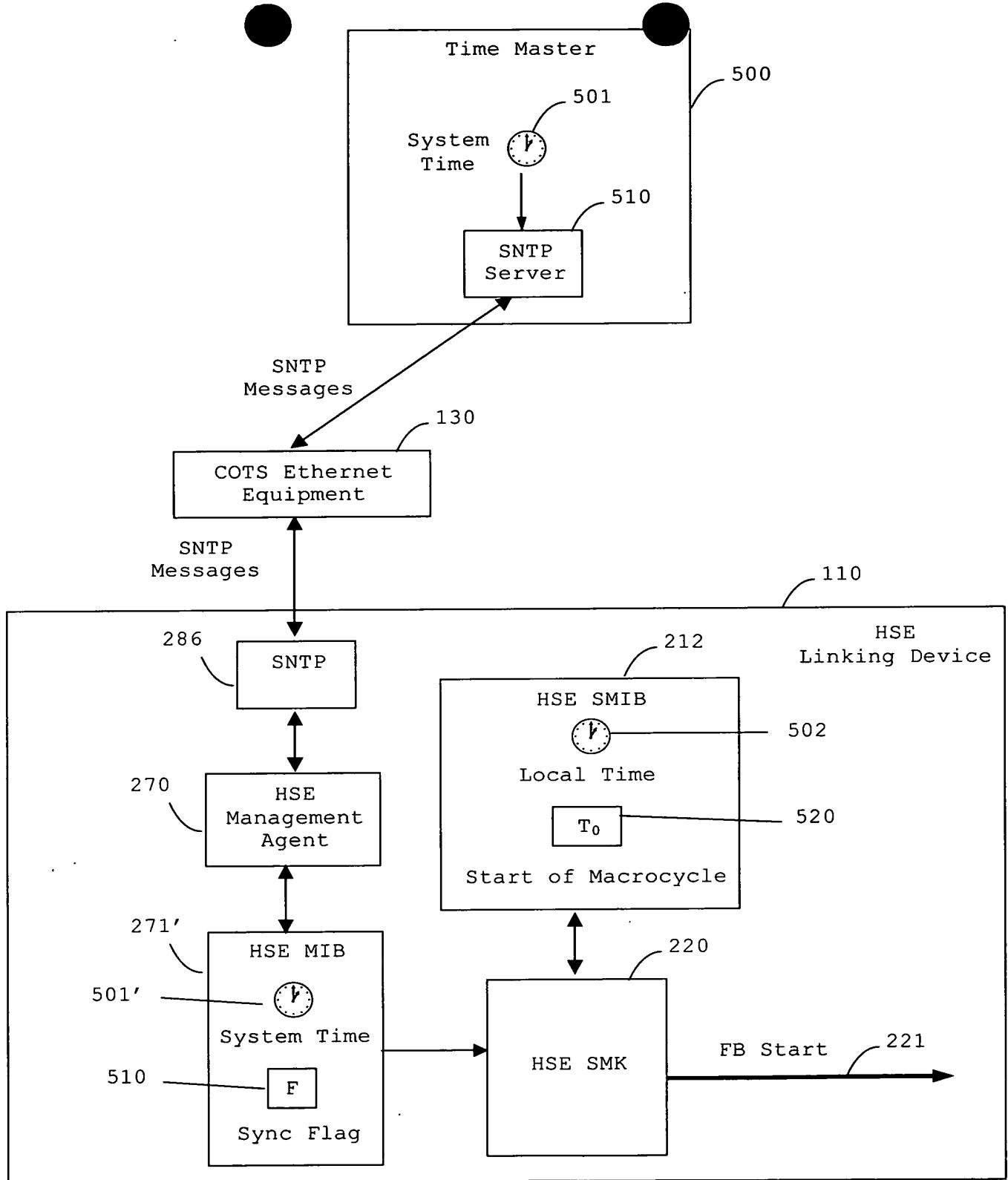


FIG. 5

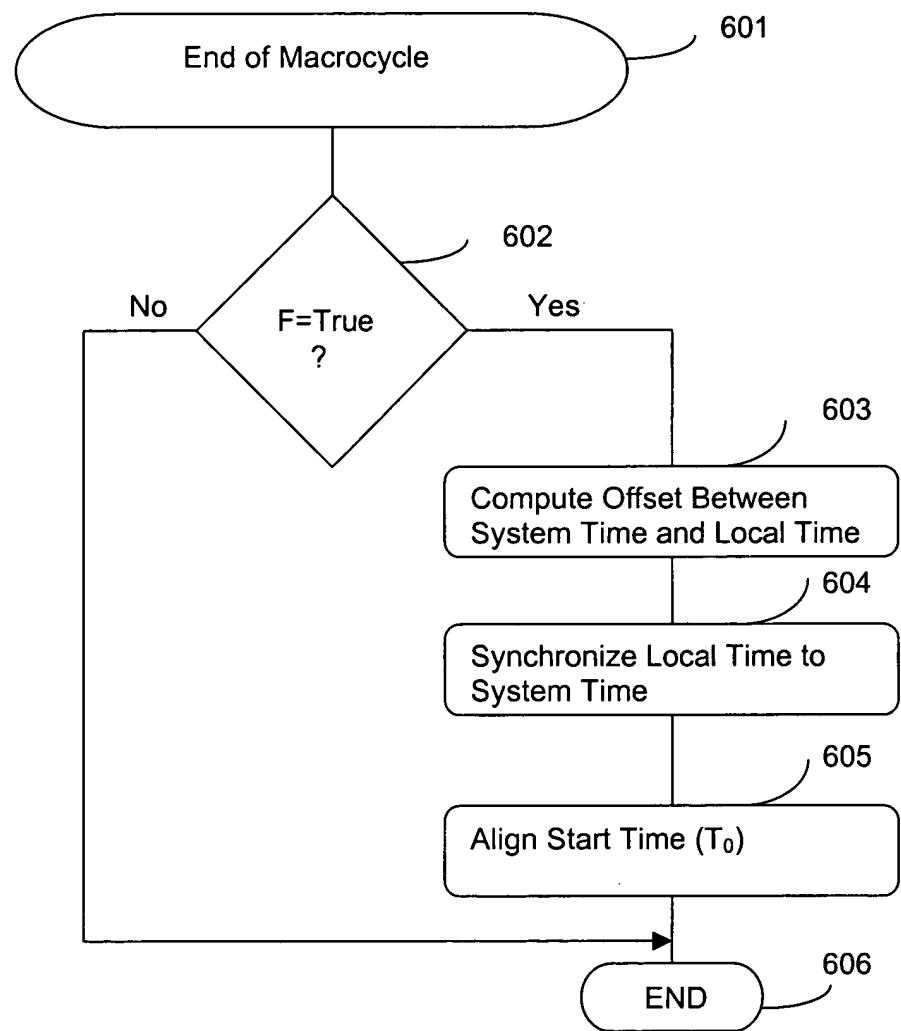


FIG. 6

Device N Macrocycle Offset Before Time Synchronization

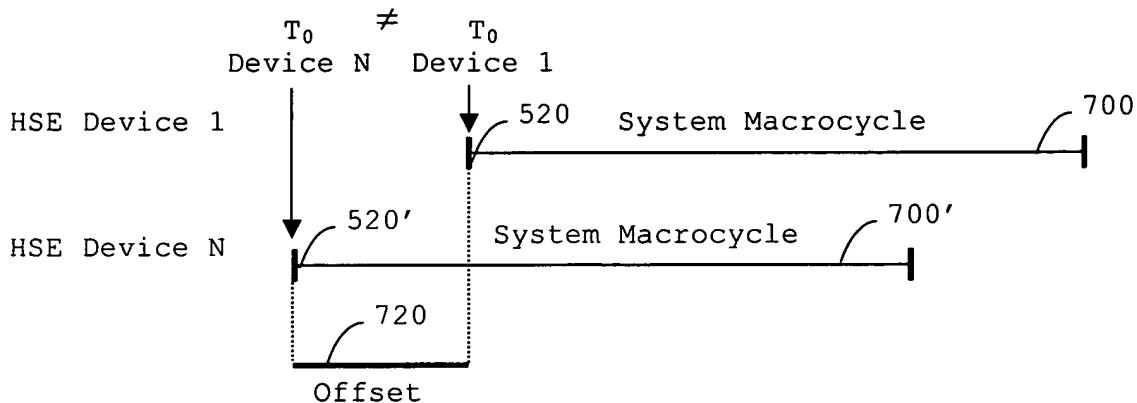


FIG. 7A

Device N Macrocycle Offset After Time Synchronization

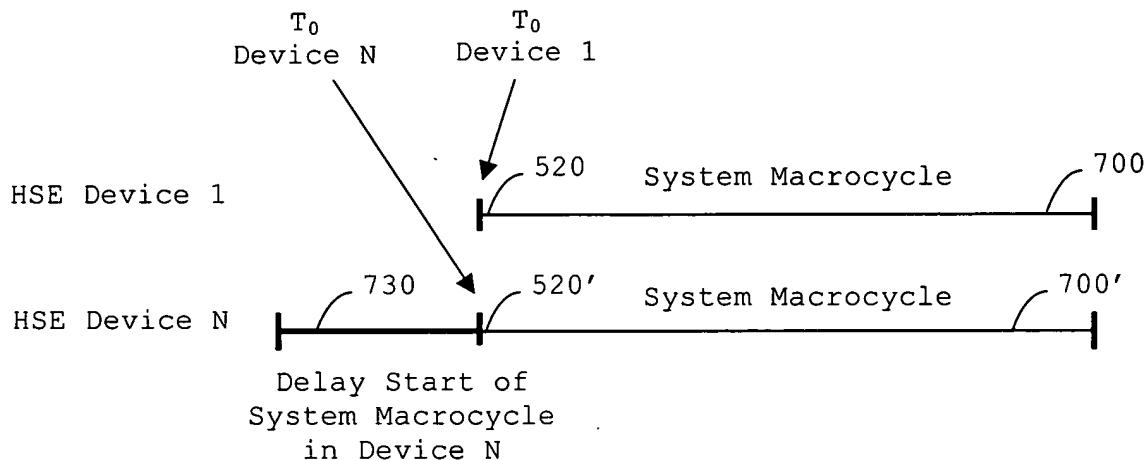


FIG. 7B

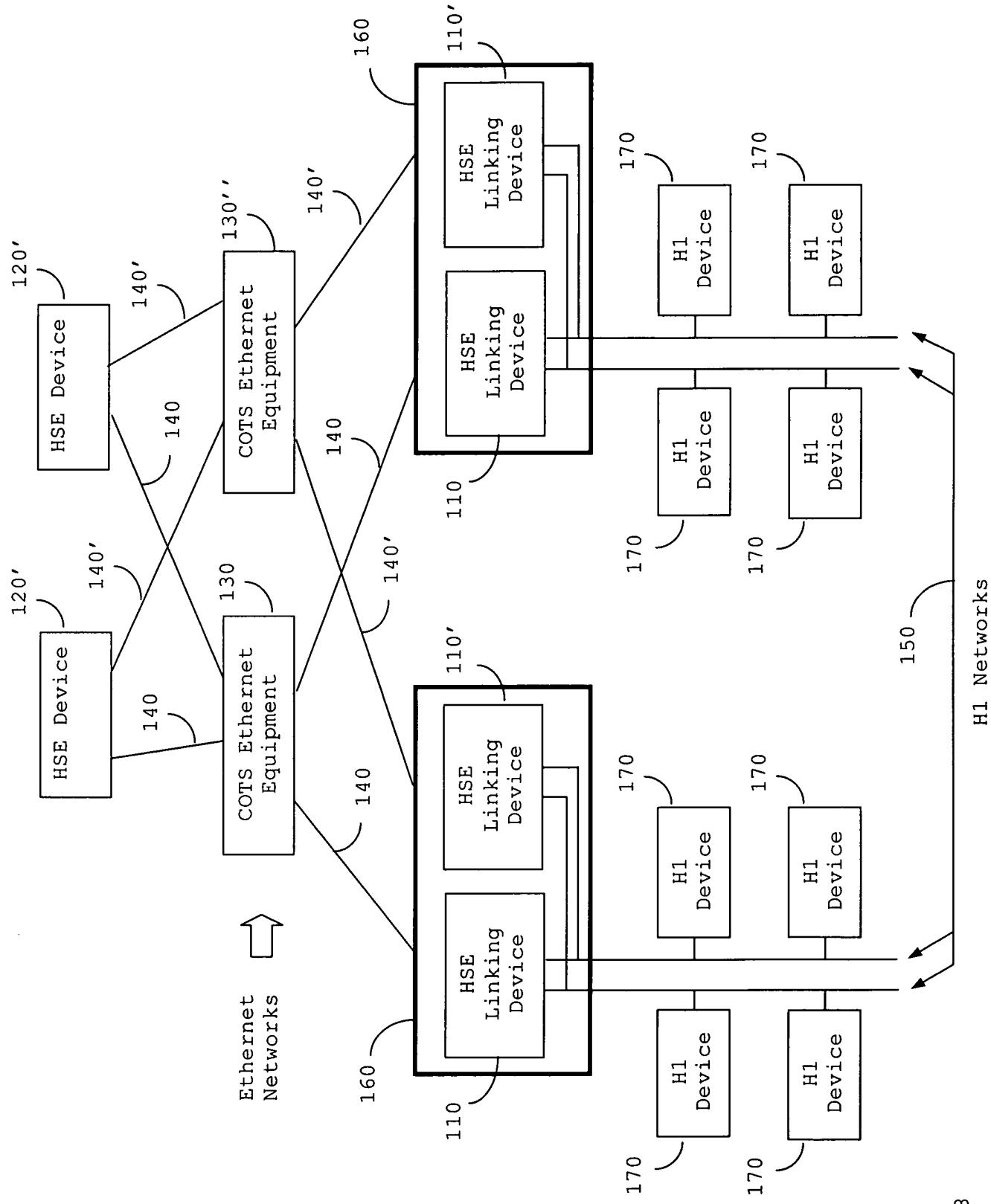


FIG. 8

H1 Networks